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10/538,806	06/28/2005	Simon Tam	124280	5681
27049	7590	02/25/2010	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 320850 ALEXANDRIA, VA 22320-4850				RAINEY, ROBERT R
ART UNIT		PAPER NUMBER		
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

OfficeAction27049@oliff.com  
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## NON-COMPLIANT AMENDMENT

### *Election/Restrictions*

1. Newly submitted claims 1-12 directed to an invention that is independent or distinct from the invention originally claimed for the following reasons: The disclosure comprises at least two species. **Species I**, represented by Fig. 10, in which switches control the application of voltages to the gate of a single current source transistor. And **Species II**, represented by Fig. 9, in which two or more current source transistors each receives a dedicated gate voltage and switches control the flow of current through the current source transistors. The original claims were either generic to both species, as in original claim 1, or specific to species I, as in original claim 4.

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution on the merits. Accordingly, claims 1-12 withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

### *Response to Amendment*

2. The amendment filed on 11/02/2009 canceling all claims drawn to the elected invention and presenting only claims drawn to a non-elected invention is non-responsive (MPEP § 821.03). The remaining claims are not readable on the elected invention because as amended, claim 1, which is the only independent claim, requires three transistors and three switches as in Species II rather than the three switches and two transistors as in Species I.

Since the above-mentioned amendment appears to be a *bona fide* attempt to reply, applicant is given a TIME PERIOD of ONE (1) MONTH or THIRTY (30) DAYS, whichever is longer, from the mailing date of this notice within which to supply the omission or correction in order to avoid abandonment. EXTENSIONS OF THIS TIME PERIOD UNDER 37 CFR 1.136(a) ARE AVAILABLE.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ROBERT R. RAINY whose telephone number is (571)270-3313. The examiner can normally be reached on Monday through Friday 8:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amare Mengistu can be reached on (571) 272-7674. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/RR/

/Amare Mengistu/  
Supervisory Patent Examiner, Art Unit 2629